

714	736
Class	Subclass
ISSUE CLASSIFICATION	

FILED UNDER 35 U.S.C. 371

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. M.H SCANNED LM3 Q.A. 58	PATENT DATE
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/831525	D F	714	736	2184	BK DTY Decady

APPLICANTS
Romain Degraeve
Philippe PardetTITLE
Method and installation for fault fault localization in an integrated circuitPTO-2040
12/89

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ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
714	736	714	819		
INTERNATIONAL CLASSIFICATION		324	523		
G 06 F	11/00	327	603		
G 06 E	3/02				
G 01 R	31/08				
G 01 R	31/28				

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	13	20	3	35	1
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<i>Cynthia Barth</i> 5/11/04 (Assistant Examiner)			NOTICE OF ALLOWANCE MAILED <i>5/12/04</i>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	ALBERT DECADY SUPERVISORY PATENT EXAMINER TECHNOLGY CENTER 2100 <i>Albert Decady</i> 5/12/04 (Primary Examiner)			ISSUE FEE Amount Due <i>\$1330</i> Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	

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